



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **SUGATANI, Shinji, et al.**

Group Art Unit: **2814**

Serial No.: **10/634,839**

Examiner: **Howard Weiss**

Filed: **August 6, 2003**

P.T.O. Confirmation No.: 8605

For. **MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE AND
SEMICONDUCTOR CHIP USING SOI SUBSTRATE**

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: February 22, 2005

Sir:

In response to the Office Action dated **September 22, 2004**, and the Advisory Action dated **February 7, 2005**, extended to **February 22, 2005** by a partial two month Petition for Extension of Time, entry of the following remarks, further examination, and reconsideration are respectfully requested.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.